


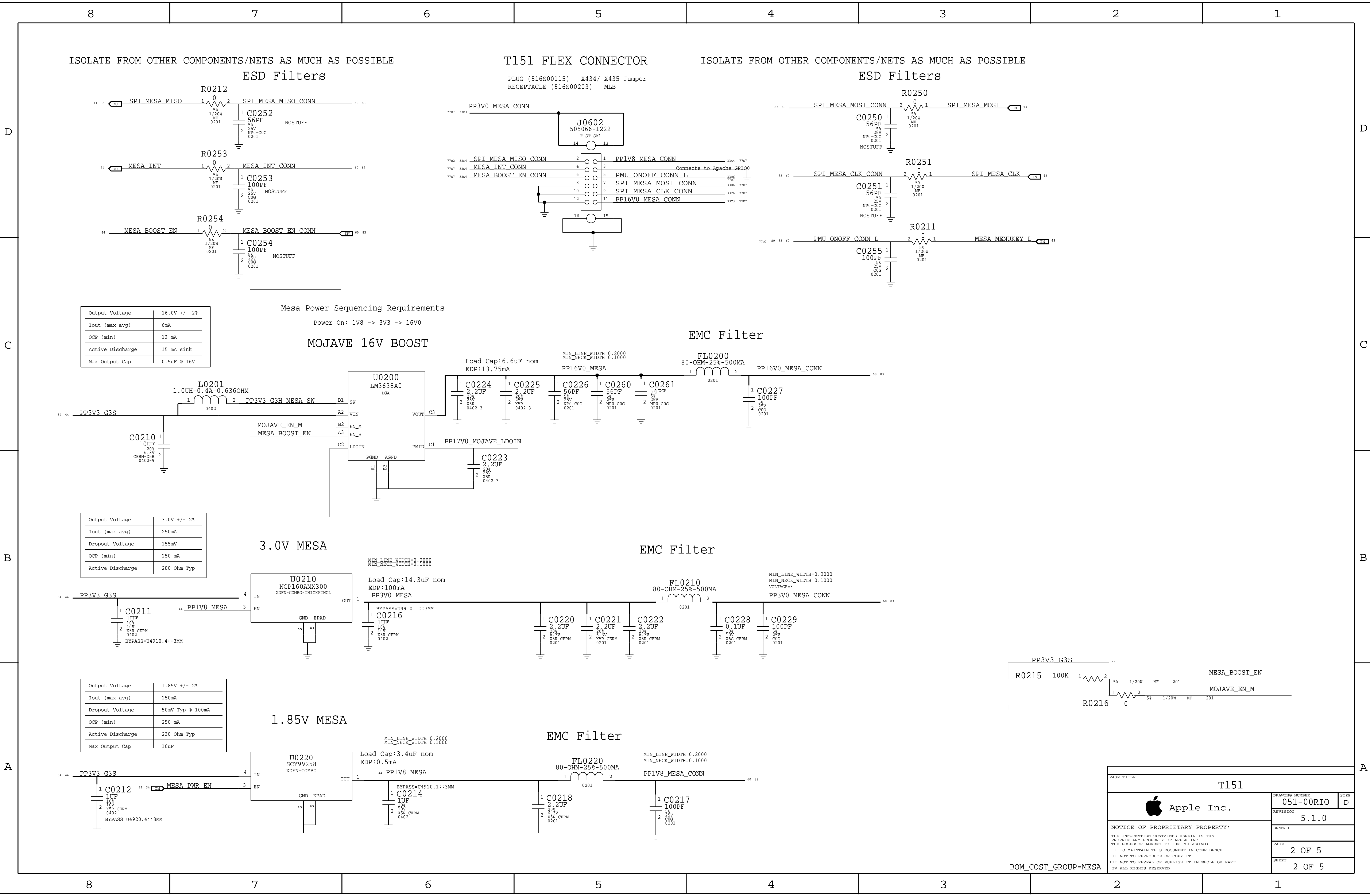
1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.


820-01124 MLB SCHEMATIC

REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE
5	0021434873	ENGINEERING RELEASED	2019-12-20

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 Apple Inc.	DRAWING NUMBER 051-00RIO		SHEET D
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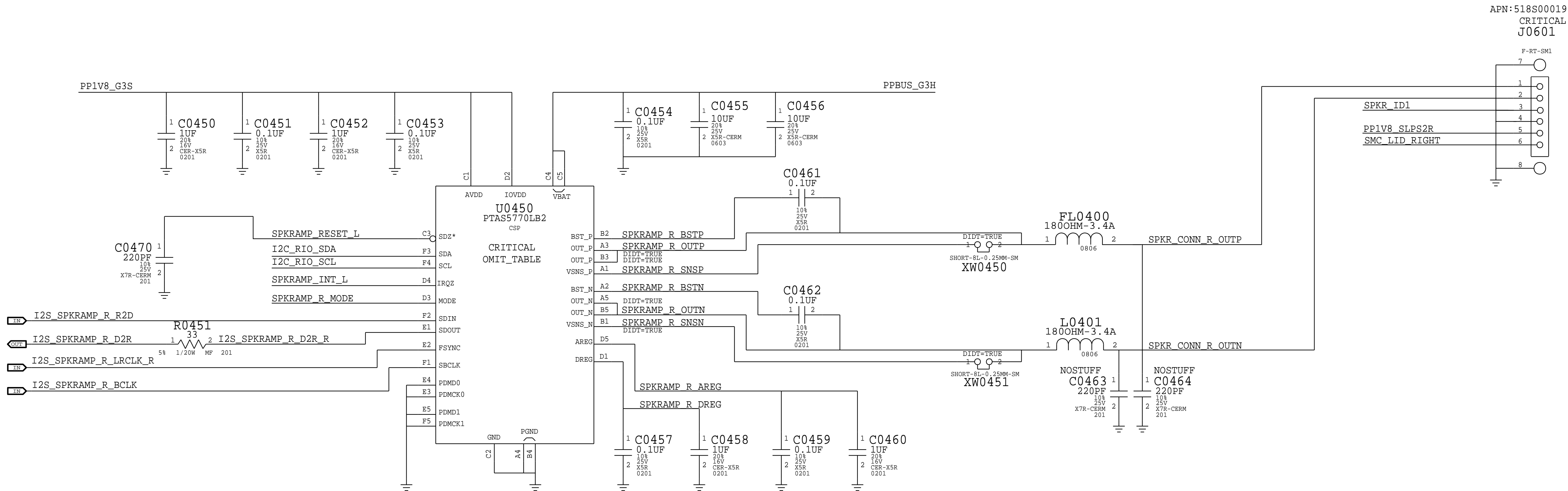
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1X MONO SPEAKER RIGHT AMPLIFIERS

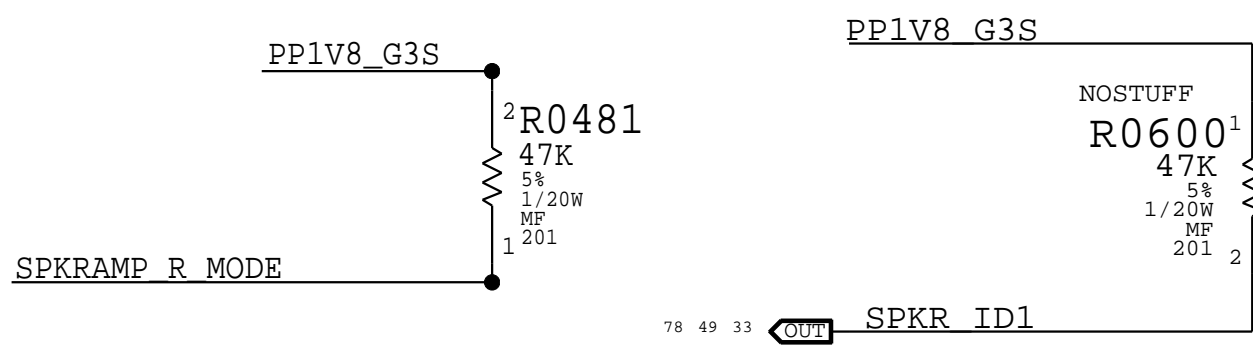
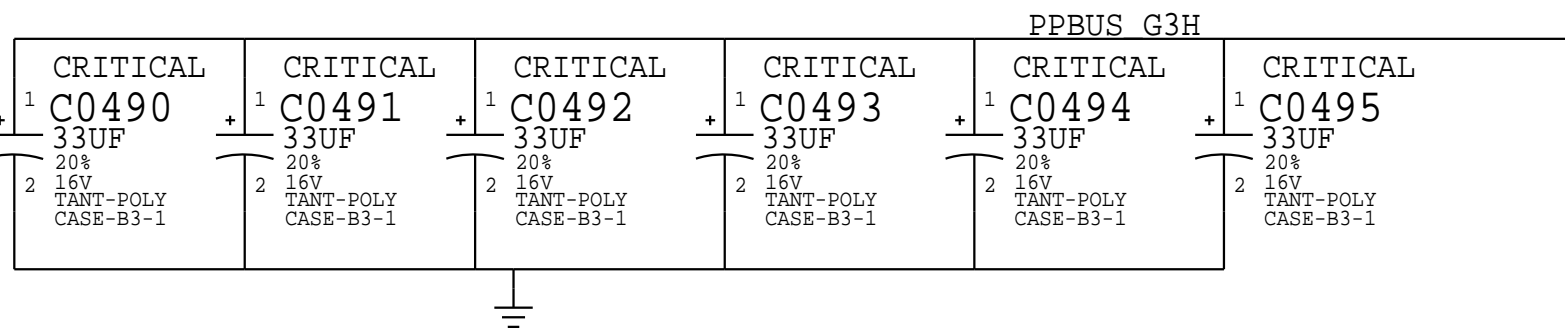
APN: 353S01252  
GAIN: 0DBFS = xxVRMS

59



Right Speaker ID

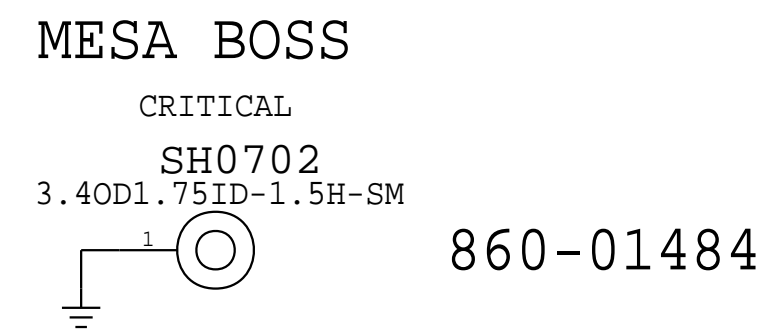
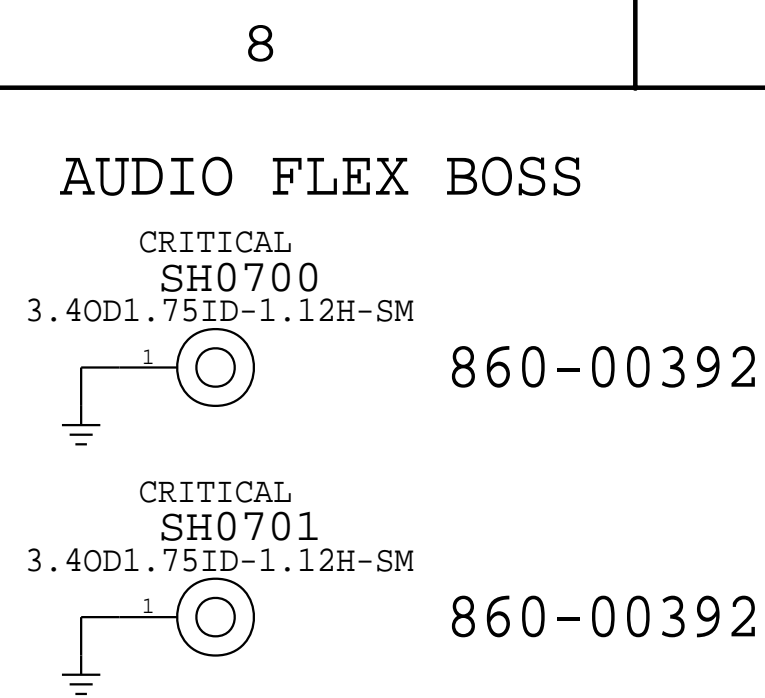
RIGHT BULK CAPACITANCE



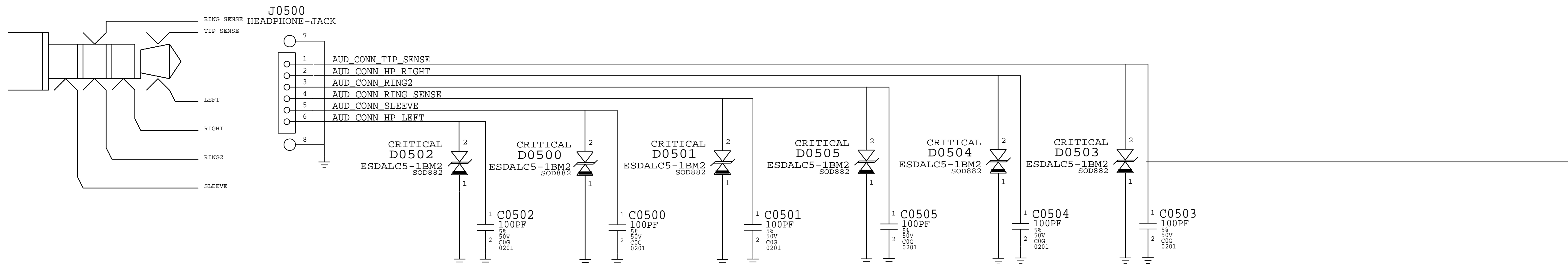
MODE PIN	I2C ADDR	CHANNEL
GND	0x31	LEFT
470 to GND	0x32	
470 to IOVDD	0x33	
2k2 to GND	0x34	
2k2 to IOVDD	0x35	
10k to GND	0x36	
10k to IOVDD	0x37	
47k to IOVDD	0x38	RIGHT

BOM\_COST\_GROUP=AUDIO

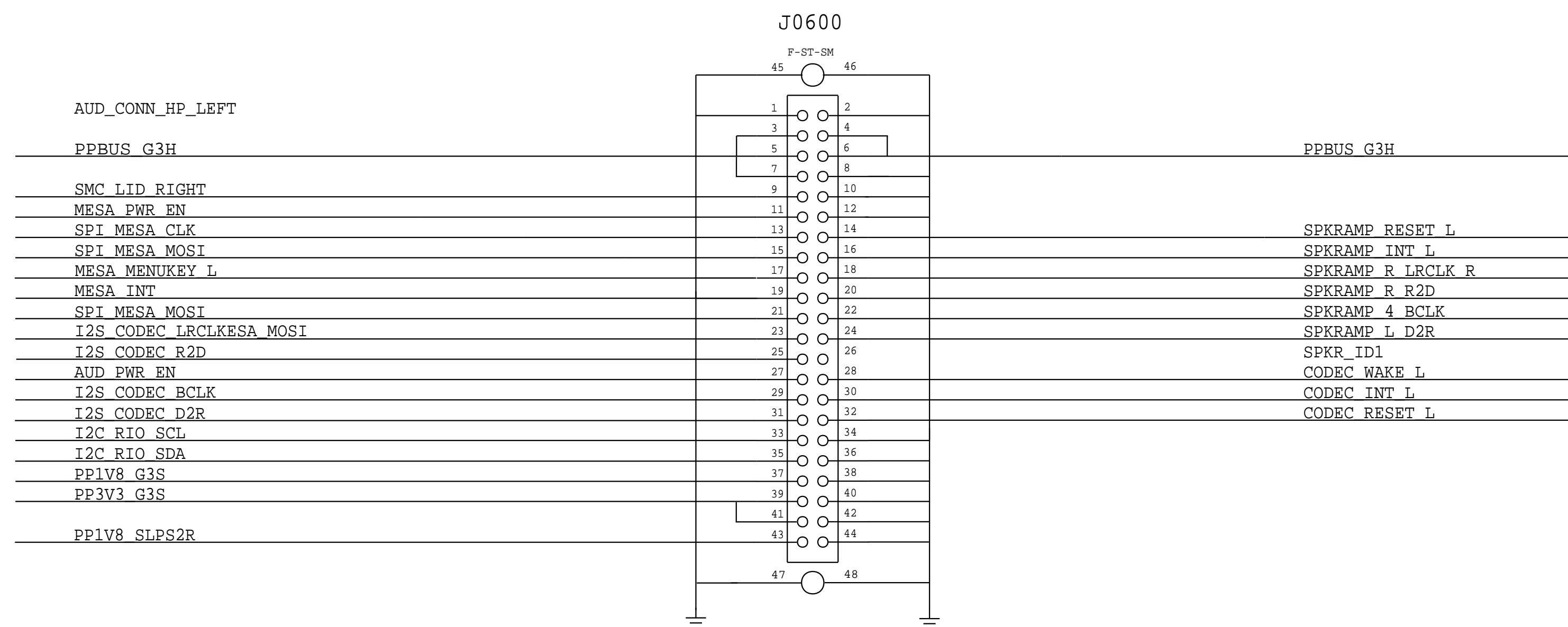
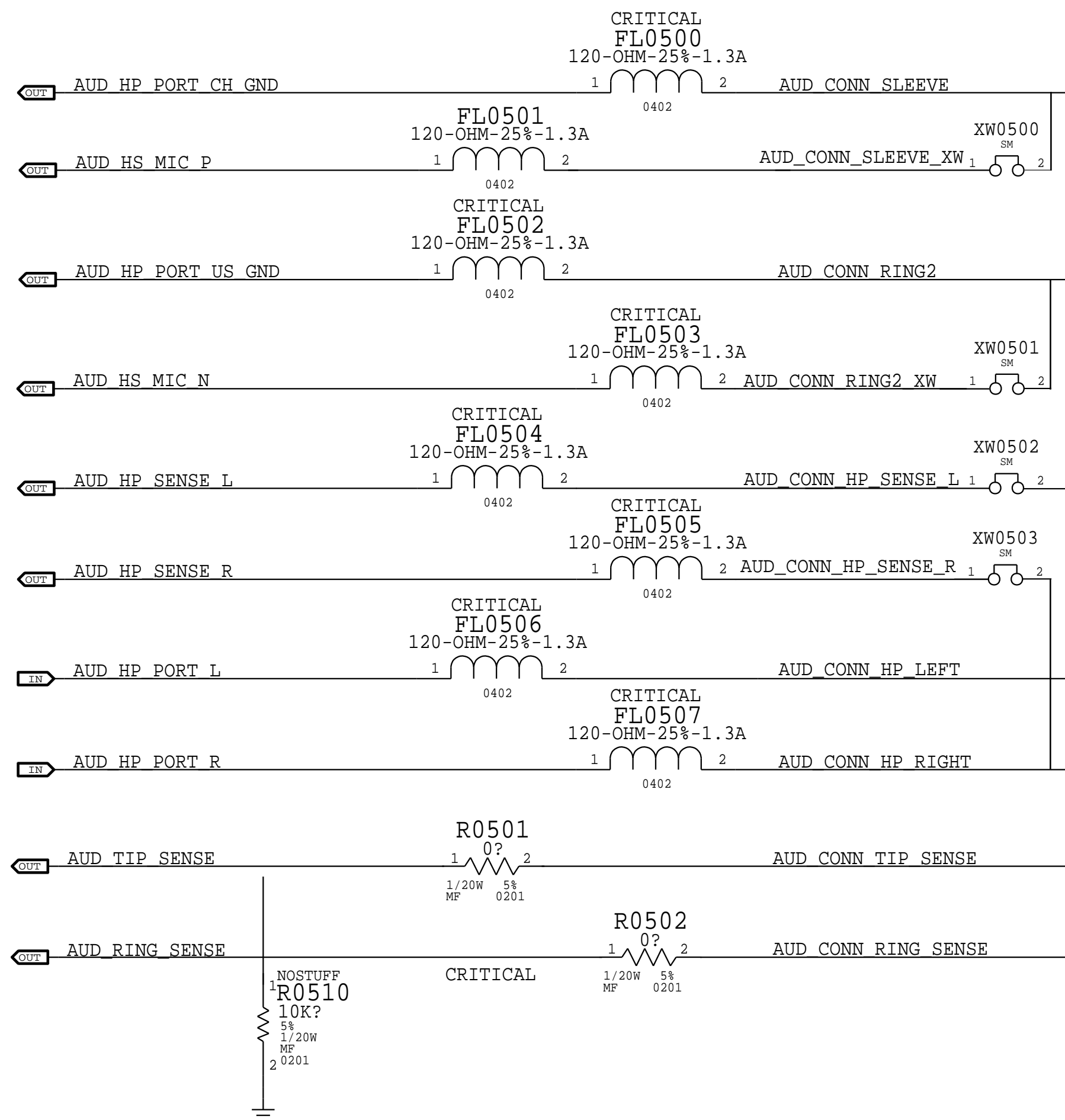
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HEADPHONE-JACK



# AUDIO JACK FLEX CONNECTOR

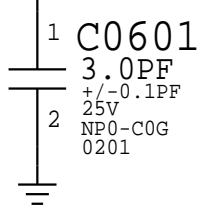
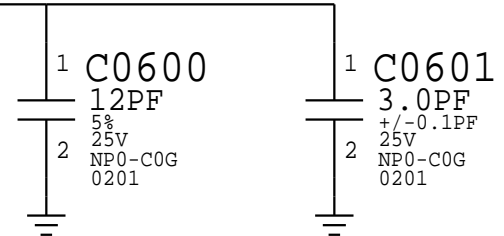


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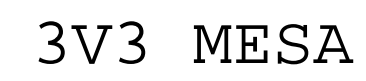
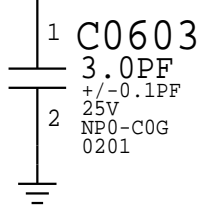
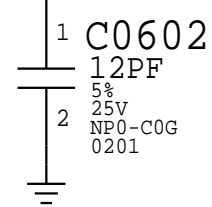
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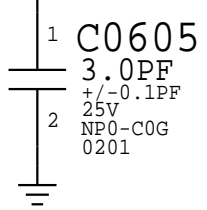
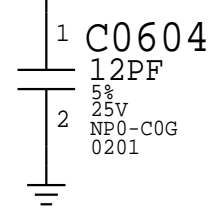
PPBUS\_G3H



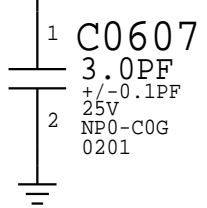
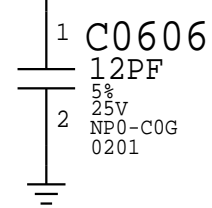
PP1V8\_GS




PP3V3 GS



PP1V8 SLPS2R



SYMC_MASTER- PAGE TITLE		SYMC_DATE-	
<h1>DESENSE 1</h1>			
 <div>Apple Inc.</div>		DRAWING NUMBER <b>051-00RIO</b>	SIZE <b>D</b>
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